

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization
International Bureau



(43) International Publication Date
2 May 2002 (02.05.2002)

PCT

(10) International Publication Number
WO 02/35894 A1

(51) International Patent Classification⁷: **H05B 41/392, 41/36, 37/03**

(74) Agent: **BOSMA, Rudolphus, H., A.; Internationaal Octrooibureau B.V.**, Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).

(21) International Application Number: **PCT/EP01/12328**

(81) Designated States (national): CN, JP.

(22) International Filing Date: 18 October 2001 (18.10.2001)

(84) Designated States (regional): European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE, TR).

(25) Filing Language: English

Published:

(26) Publication Language: English

— with international search report
— before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments

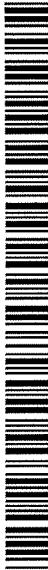
(30) Priority Data:
00203796.8 27 October 2000 (27.10.2000) EP

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

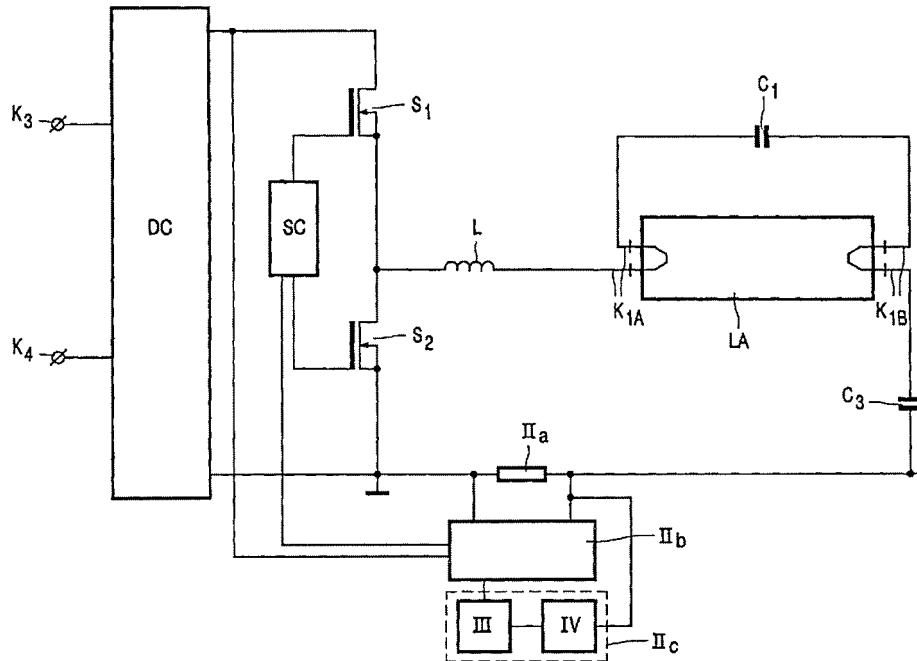
(71) Applicant: **KONINKLIJKE PHILIPS ELECTRONICS N.V. [NL/NL]**; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).

(72) Inventors: **BEIJ, Marcel**; Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL). **BUIJ, Arnold, W.**; Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).

(54) Title: CIRCUIT ARRANGEMENT



WO 02/35894 A1



(57) Abstract: A ballast circuit for operating a discharge lamp is equipped with a timer for measuring the service life of the lamp, and with means for increasing the power supplied to the lamp as the number of burning hours increase. The decrease in efficiency associated with the increase in burning hours is compensated thereby.

Circuit arrangement

The invention relates to a circuit arrangement for energizing a lamp, comprising

- input terminals which are to be connected to a supply voltage source,
- 5 a first circuit part I coupled to the input terminals for generating a current through the lamp from a supply voltage supplied by the supply voltage source,
- a second circuit part II coupled to the first circuit part I for setting the power consumed by the lamp to a desired value.

10

Such a circuit arrangement is disclosed in EP 0430357. In the known circuit arrangement, the power consumed by the lamp is regulated by measuring the actual lamp power and comparing this with the desired value. The result of this comparison is used to influence the operating condition of the first circuit part I in such a manner that the actual power consumed by the lamp is continuously substantially equal to the desired value of the lamp power. Viewed over a small number of operating hours of the lamp, such a substantially constant value of the power consumed by the lamp also means that the luminous flux of the lamp is substantially constant. However, viewed over a comparatively large number of operating hours, the luminous flux of the lamp decreases, at a constant lamp power, as a result of aging of the lamp. Apart from said decrease of the luminous flux as a result of aging of the lamp, the overall quantity of light may also decrease as a result of, for example, fouling of the luminaire accommodating the lamp. Often, such a reduction of the total quantity of light is taken into account in that the desired value of the power consumed by the lamp is set to a comparatively high value. As a result, although the quantity of light emitted by the lamp has decreased after a comparatively large number of burning hours, it still meets the requirements imposed, for example, for safety reasons. A drawback of dealing with the problem in such a way resides in that, during this comparatively large number of burning hours, the power consumption of the lamp is higher than would be necessary to generate a

quantity light that satisfies the prevailing safety requirements. As a result, the operation of the lamp using the known circuit arrangement is comparatively inefficient.

It is an object of the invention to provide a circuit arrangement which enables
5 a lamp to generate a quantity of light, throughout its service life, which satisfies the requirements to be imposed, while the lamp operates comparatively efficiently throughout the service life.

To achieve this, a circuit arrangement of the type mentioned in the opening
10 paragraph is characterized in accordance with the invention in that the second circuit part comprises a timer for measuring the total number of burning hours of the lamp, and a third circuit part III, coupled to said timer, for setting the power consumption of the lamp at the desired value in dependence upon the number of burning hours of the lamp.

In the course of the service life of the lamp, the third circuit part III increases
15 the desired value of the power consumed by the lamp. As a result, the reduction of the amount of light generated due to aging and fouling is at least partly compensated for. If a circuit arrangement in accordance with the invention is used, the desired value of the power consumed by the lamp can be set, immediately after the lamp has come into operation (i.e.
20 after zero burning hours), at a value such that the amount of light supplied by the lamp is sufficient, yet not much larger, to satisfy, for example, safety requirements. Before the amount of light supplied by the lamp decreases as a result of aging and fouling to a level below that required by safety requirements, the desired value of the power consumed by the lamp is increased by the third circuit part III. It is thus achieved that, throughout its service
25 life, the lamp consumes approximately as much power as it needs to produce a desired/required amount of light. As a result, the lamp operates very efficiently. The increase of the desired value of the lamp power can take place continuously. Alternatively, such an increase can also take place in steps after a predetermined number of burning hours. In the latter case, said third circuit part advantageously comprises a memory for establishing a table
30 that determines the relation between the overall number of burning hours and the desired value of the power consumed by the lamp. Said third circuit part also advantageously comprises a microprocessor enabling such a table to be read and the content of the timer to be monitored.

These and other aspects of the invention will be apparent from and elucidated with reference to the embodiment(s) described hereinafter.

5

In the drawings:

Fig. 1 shows an example of a circuit arrangement in accordance with the invention to which a lamp is connected, and

Fig. 2 shows the power consumed and the luminous flux of a lamp energized by means of the example shown in Fig. 1 as a function of the number of burning hours of the lamp.
10

In Fig. 1, K3 and K4 are input terminals which are to be connected to a supply voltage source. In the case of the example shown in Fig. 1, this supply voltage source supplies a low-frequency AC voltage. Input terminals K3 and K4 are connected to respective 15 inputs of circuit part DC. Circuit part DC is a circuit part for generating a substantially constant DC voltage from a low-frequency AC voltage. A first output of the circuit part DC is connected to a second output by means of a series arrangement of a first switching element S1 and a second switching element S2. A control electrode of the first switching element S1 is coupled to a first output of circuit part SC. A control electrode of the second switching 20 element S2 is coupled to a second output of circuit part SC. Circuit part SC is a control circuit for rendering the switching elements S1 and S2 alternately conducting and non-conducting.

Switching element S2 is shunted by a series arrangement of coil L, lamp terminal K1A, lamp LA, lamp terminal K1B, capacitor C3 and sensor IIa, which sensor is formed by an ohmic resistance. The lamp LA is shunted by capacitor C1. Coil L, lamp terminal K1A, lamp LA, lamp terminal K1B, capacitor C3, sensor IIa and capacitor C1 jointly form a load branch. Respective ends of sensor IIa are connected to a first input and a second input of circuit part IIb. A third input of circuit part IIb is connected to the first output of circuit part DC. An 25 output of circuit part IIb is connected to an input of the circuit part SC. A junction point of sensor IIa and capacitor C3 is connected to an input of circuit part IV. Circuit part IV is a timer for measuring the total number of burning hours of the lamp. An output of circuit part IV is connected to an input of a third circuit part III for setting the power consumption of the lamp at the desired value in dependence upon the number of burning hours of the lamp. Circuit part IV and third circuit part III jointly form a circuit part IIc. Circuit part IIb, sensor 30

IIa and circuit part IIc jointly form a circuit part II for setting the power consumed by the lamp at a desired value. All components and circuit parts of the example shown in Fig. 1, with the exception of the second circuit part II and the lamp LA, jointly form a first circuit part I for generating a current through the lamp from the low-frequency AC voltage supplied by the supply voltage source. In this example, the third circuit part III is formed by a microprocessor.

The operation of the example shown in Fig. 1 is as follows.

If the input terminals K3 and K4 are connected to a supply voltage source supplying a low-frequency AC voltage, the circuit part DC generates a substantially constant DC voltage from this low-frequency AC voltage, said DC voltage being present between the outputs of the circuit part DC. The circuit part SC renders the first switching element S1 and the second switching element S2 successively conducting and non-conducting at a frequency f. As a result, a substantially square-wave voltage of frequency f is present at a junction point of the switching elements. Under the influence of said substantially square-wave voltage, an alternating current of frequency f flows in the load branch. A voltage whose amplitude is proportional to the instantaneous amplitude of the current in the load branch is present between the first and the second input of the circuit part IIb. A signal that is a measure of the DC voltage generated by the circuit part DC is present on the third input of the circuit part IIb. By means of the signals present on the first, the second and the third input, the circuit part IIb generates a first signal that is a measure of the actual power consumed by the lamp. A second signal that is a measure of a desired value of the power consumed by the lamp is present on the output of the third circuit part III and hence on the fourth input of the circuit part IIb. The circuit part IIb compares the first signal with the second signal and influences, via the output of circuit part IIb and the input of circuit part SC, the frequency and/or the duty cycle with which the switching elements are rendered conducting and non-conducting, in such a manner that the actual power consumed by the lamp is substantially equal to the desired value. The timer formed by the circuit part IV counts the number of burning hours of the lamp LA as long as the voltage across sensor IIa indicates that the lamp LA is in operation. If the content of the timer has increased by a predetermined number of burning hours, then the microprocessor forming the third circuit part III increases its output signal to a predetermined value established in a table in a memory forming part of the microprocessor. This table determines the relation between the number of burning hours and the desired value of the power consumed by the lamp. It is thus achieved that, throughout its service life, the

lamp generates an amount of light that meets the requirements and/or corresponds to the amount desired, yet does not substantially exceed said required or desired amount of light, so that the power consumption of the lamp, at any moment in time, is comparatively small.

5 In Fig. 2, the luminous flux of the lamp, expressed as a percentage of the maximum luminous flux of the lamp, is plotted along the left, vertical axis. The power consumed by the lamp, expressed in Watt, is plotted along the right, vertical axis. The number of burning hours, expressed in units of hours, is plotted along the horizontal axis. The curve GLO indicates the luminous flux of a low-pressure mercury discharge lamp having a 10 rated power of 60 Watts as a function of the number of burning hours. This luminous flux is expressed as a percentage of the maximum luminous flux and increases as a function of the number of burning hours. This increase can be attributed to the fact that the maximum luminous flux of the lamp decreases as a result of aging. The curve LP shows the power supplied to the lamp as a function of the number of burning hours. The Figure also shows that 15 this curve is an increasing function of the number of burning hours. The curve DLL shows both the desired amount of light and the actual amount of light emitted by a lamp energized by means of a circuit arrangement as shown in Fig. 1. The Figure shows that the curve DLL is a substantially horizontal line. Immediately after the lamp has been put into operation (i.e. after zero burning hours), the power consumed by the lamp is set at approximately 42 Watts. 20 At this power, the lamp supplies 70% of the maximum luminous flux that the lamp can supply (at zero burning hours). After 15,000 burning hours, the power consumed by the lamp is 57 Watts, and the lamp supplies approximately the maximum luminous flux (i.e. that the lamp is capable of producing after 15,000 burning hours). The Figure shows that the average power consumed by the lamp is approximately 49.5 Watts. Since the maximum power 25 consumed by the lamp is approximately 57 Watts, the measure in accordance with the invention enables a saving in energy to be achieved that is approximately equal to 15,000 hours * (57 Watts – 49.5 Watts) = 112.5 kilowatthour.

CLAIMS:

1. A circuit arrangement for energizing a lamp, comprising
 - input terminals which are to be connected to a supply voltage source,
 - a first circuit part I coupled to the input terminals for generating a current through the lamp from a supply voltage supplied by the supply voltage source,
 - a second circuit part II coupled to the first circuit part I for setting the power consumed by the lamp at a desired value, characterized in that the second circuit part comprises a timer for measuring the total number of burning hours of the lamp, and a third circuit part III, coupled to said timer, for setting the power consumption of the lamp at desired value in dependence upon the number of burning hours of the lamp.
2. A circuit arrangement as claimed in claim 1, wherein the third circuit part increases the desired value of the power consumed by the lamp in a step-by-step manner after a predetermined number of burning hours of the lamp.
3. A circuit arrangement as claimed in claim 2, wherein the third circuit part comprises a memory for establishing a table that determines the relation between the total number of burning hours and the desired value of the power consumed by the lamp.
4. A circuit arrangement as claimed in claim 1, 2 or 3, wherein the third circuit part comprises a microprocessor.

1/2

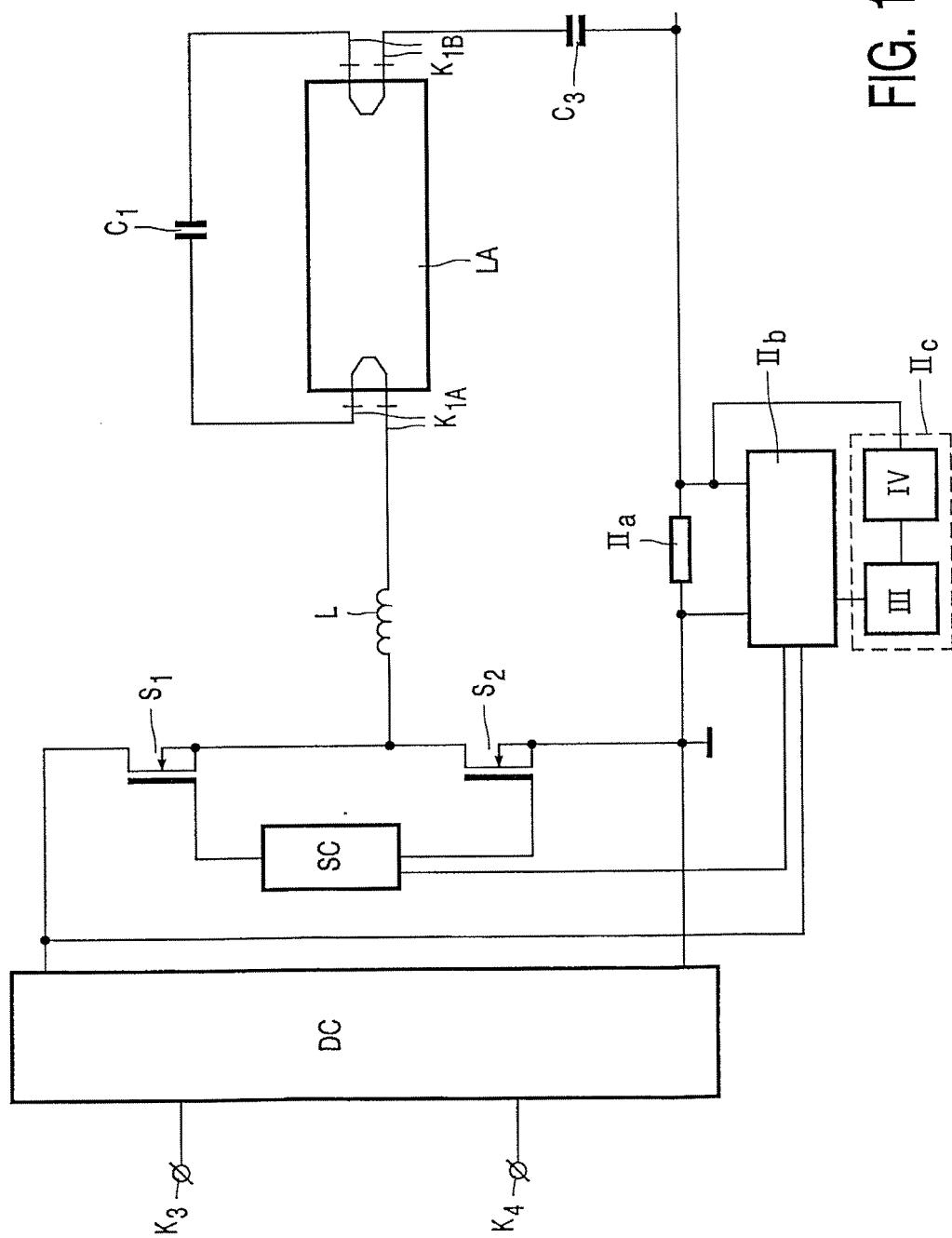


FIG. 1

2/2

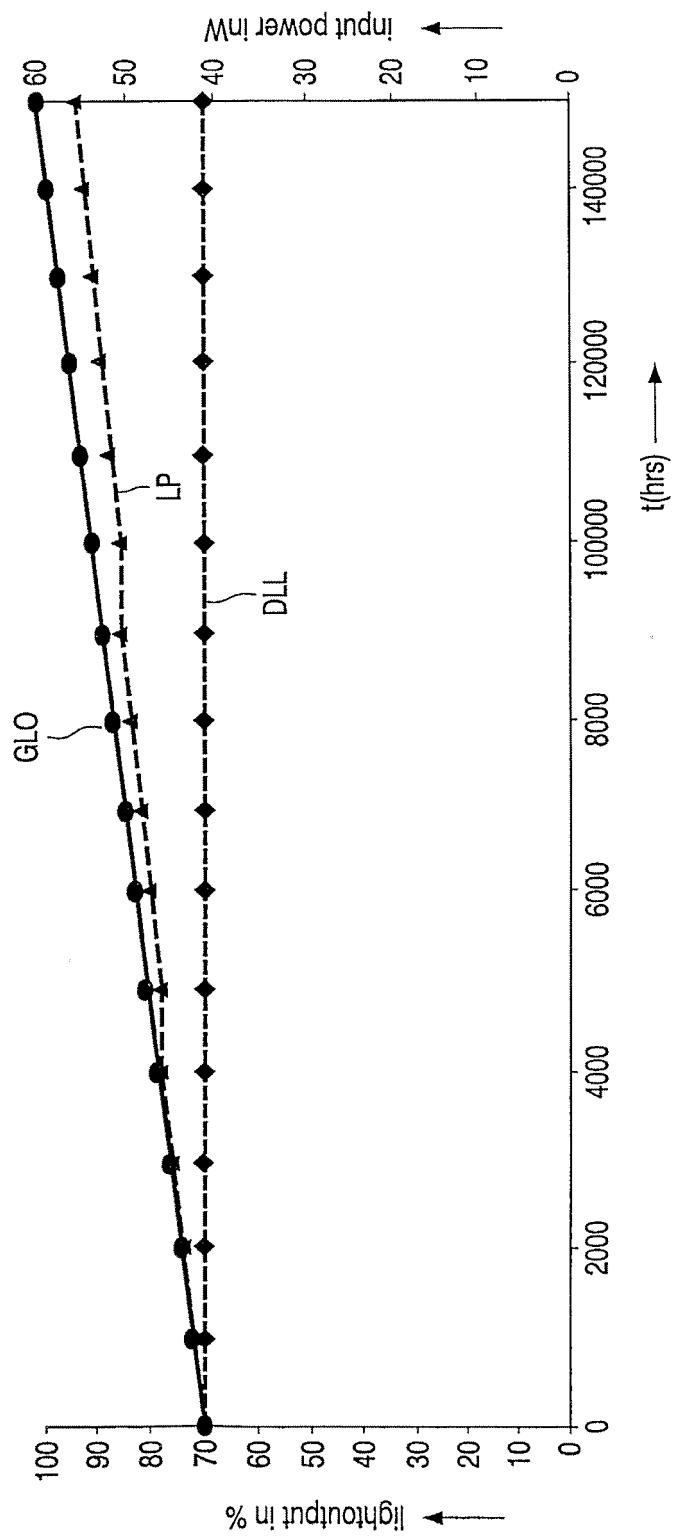


FIG. 2

INTERNATIONAL SEARCH REPORT

I International Application No
PCT/EP 01/12328

A. CLASSIFICATION OF SUBJECT MATTER

IPC 7 H05B41/392 H05B41/36 H05B37/03

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 7 H05B

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	DE 195 40 326 A (BOSCH GMBH ROBERT) 30 April 1997 (1997-04-30) column 1, line 18 -column 1, line 23 column 1, line 49 -column 1, line 53 column 3, line 32 -column 4, line 48 column 4, line 64 -column 5, line 26; figures 2-5 ----	1-4
X	EP 0 952 757 A (MANNESMANN VDO AG) 27 October 1999 (1999-10-27) abstract column 6, line 10 -column 6, line 21; figure 1 ---- -/-	1-4

Further documents are listed in the continuation of box C.

Patent family members are listed in annex.

° Special categories of cited documents :

- *A* document defining the general state of the art which is not considered to be of particular relevance
- *E* earlier document but published on or after the international filing date
- *L* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- *O* document referring to an oral disclosure, use, exhibition or other means
- *P* document published prior to the international filing date but later than the priority date claimed

T later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

X document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

Y document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.

*& document member of the same patent family

Date of the actual completion of the International search

10 April 2002

Date of mailing of the International search report

17/04/2002

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,
Fax: (+31-70) 340-3016

Authorized officer

Speiser, P

INTERNATIONAL SEARCH REPORTI tional Application No
PCT/EP 01/12328**C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT**

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5 493 183 A (KIMBALL ROBERT A) 20 February 1996 (1996-02-20) abstract column 2, line 38 -column 2, line 46; figures 1,3 -----	1-4
X,P	DE 200 11 572 U (GUENTHER RALF PETER ;STAUDE BERNHARD (DE)) 22 March 2001 (2001-03-22) page 1, line 1 -page 2, line 21 -----	1-4
A,P	DE 100 08 778 A (DEURAG AG GLARUS) 13 September 2001 (2001-09-13) abstract; figure 1 -----	1-4

INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No
.../EP 01/12328

Patent document cited in search report	Publication date		Patent family member(s)		Publication date
DE 19540326	A	30-04-1997	DE FR JP	19540326 A1 2740647 A1 9180886 A	30-04-1997 30-04-1997 11-07-1997
EP 0952757	A	27-10-1999	DE EP	19818621 A1 0952757 A2	28-10-1999 27-10-1999
US 5493183	A	20-02-1996	NONE		
DE 20011572	U	22-03-2001	DE	20011572 U1	22-03-2001
DE 10008778	A	13-09-2001	DE	10008778 A1	13-09-2001